

# ACKNOWLEDGEMENT RECEIPT

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## Title of Invention

METHOD FOR DESIGNING AN INTEGRATED CIRCUIT HAVING  
MULTIPLE VOLTAGE DOMAINS

Submission Type : Utility Patent Filing

Application Number:

10/707068



EFS ID:

50883

Server Response:

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ISVR1	Submission was successfully submitted - Even if Informational or Warning Messages appear below, please do not resubmit this application
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WGEN24	Filename= all XMLs having <invention-title> BusinessRule= Common elements across multiple XML documents within the submission package do not have synchronized data content. #Supporting Msg:<invention-title> not the same or having font fomats.

First Named Applicant: Joseph Iadanza

Attorney Docket Number: BUR920030126US1

Timestamp: 2003-11-19 11:26:44 EDT

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